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1           IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

2           Application Serial Number . . . . . 09/292,132  
3           Filing Date . . . . . April 14, 1999  
4           Inventor . . . . . Salman Akram et al.  
5           Assignee . . . . . Micron Technology, Inc.  
6           Group Art Number . . . . . 2812  
7           Examiner . . . . . S. Mulpuri  
8           Attorney's Docket No. . . . . MI22-1171  
9           Title: Methods of Forming a Transistor Gate

10           INFORMATION DISCLOSURE STATEMENT

11           References -- See Attached Form PTO-1449

12           The attached form PTO-1449 is submitted in compliance  
13           with 37 CFR §1.56. No admission is made regarding whether all the  
14           submitted references are prior art.

15           Respectfully submitted,

16           Dated: 1/27/00

17           Attorney:

18             
19           James D. Shaurette  
20           Reg. # 39,833

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